

**Abstract of the Disclosure**

An electronic system according to various aspects of the present invention includes a memory module configured to operate in multiple modes. The memory module includes at least one connection configured to perform different functions when the memory module is operating in different modes. In one embodiment, the memory operates in a normal mode and an SPD mode. While in SPD mode, the connection performs one or more SPD functions, such as operating as an SMBus interface connection. In normal mode, the connection serves a function associated with normal operation of the module, such as addressing functions.

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10073475-021102